

Spring 2023 ECE Class Schedule											
Subj	CRS	Course Title	Cmp	Sctn	Cre	Days	Start Time	End Time	Durat	Room Request	Instr
ESE	118	Digital Logic Design	LEC	1		TUTH	6:30 PM	7:50 PM	80	FREY 104	Peter Milder
ESE	118	Digital Logic Design	LAB	L01	4	W	6:05 PM	9:05 PM	180	HVY ENGR LAB 235	Peter Milder
ESE	118	Digital Logic Design	LAB	L02	4	M	6:05 PM	9:05 PM	180	HVY ENGR LAB 235	Peter Milder
ESE	118	Digital Logic Design	LAB	L03	4	M	1:00 PM	4:00 PM	180	HVY ENGR LAB 235	Peter Milder
ESE	118	Digital Logic Design	LAB	L04	4	W	1:00 PM	4:00 PM	180	HVY ENGR LAB 235	Peter Milder
ESE	118	Digital Logic Design	LAB	L05	4	F	10:30 AM	1:30 PM	180	HVY ENGR LAB 235	Peter Milder
ESE	121	Introduction to Audio Systems	LEC	1	3	TUTH	8:00 AM	9:20 AM	80	Light Engineering	David Westerf
						TUTH	8:00 AM	9:20 AM		Online	Jan Folkson
ESE	123	Intro to Elec and Compu Engrng	LEC	1		MW	2:40 PM	4:00 PM		Library W4550	David Westerf
ESE	123	Intro to Elec and Compu Engrng	LAB	2		TH	11:45 AM	2:35 PM		Lt Engr 283	David Westerf
ESE	123	Intro to Elec and Compu Engrng	LAB	3		TH	8:00 AM	10:50 AM		Lt Engr 283	David Westerf
ESE	123	Intro to Elec and Compu Engrng	LAB	4		W	11:45 AM	2:35 PM		Lt Engr 283	David Westerf
ESE	124	Programming Fundamentals	LEC	1		TUTH	1:15 PM	2:35 PM	80	LGT ENGR 102	Alexa Doboli
ESE	124	Programming Fundamentals	LAB	L01	4	W	8:00 AM	10:50 AM	170	LGT ENGR Lab 281	Alexa Doboli
ESE	124	Programming Fundamentals	LAB	L02	4	TU	9:45 AM	12:35 PM	170	LGT ENGR Lab 281	Alexa Doboli
ESE	124	Programming Fundamentals	LAB	L03	4	TH	9:45 AM	12:35 PM	170	LGT ENGR Lab 281	Alexa Doboli
ESE	124	Programming Fundamentals	LAB	L04	4	M	12:00 PM	2:50 PM	170	LGT ENGR Lab 281	Alexa Doboli
ESE	124	Programming Fundamentals	LAB	L05	4	F	11:45 AM	2:35 PM	170	LGT ENGR Lab 281	Alexa Doboli
ESE	124	Programming Fundamentals	LAB	L06	4	M	3:00 PM	5:50 PM	170	LGT ENGR Lab 281	Alexa Doboli
ESE	124	Programming Fundamentals	LAB	L07	4	F	3:00 PM	5:50 PM	170	LGT ENGR Lab 281	Alexa Doboli
ESE	124	Programming Fundamentals	LAB	L09	4	W	11:30 AM	2:35 PM	185	LGT ENGR Lab 281	Alexa Doboli
ESE	224	Adv. Prog. & Data Structures	LEC	1		TUTH	8:00 AM	9:20 AM	80	FREY 217	Alexa Doboli
ESE	224	Adv. Prog. & Data Structures	LAB	L01	4	TU	3:00 PM	5:50 PM	170	LGT ENGR Lab 281	Alexa Doboli
ESE	224	Adv. Prog. & Data Structures	LAB	L02	4	M	8:00 AM	10:50 AM	230	LGT ENGR Lab 281	Alexa Doboli
ESE	224	Adv. Prog. & Data Structures	LAB	L03	4	TH	3:00 PM	5:50 PM	170	LGT ENGR Lab 281	Alexa Doboli

ESE	271	Electrical Circuit Analysis	LEC	1	3	TUTH	11:30 AM	12:50 PM	80		Harbans Dhad
ESE	272	Electronics	LEC	1		TUTH	6:30 PM	7:50 PM		LGT ENGR 102	David Westerf
ESE	272	Electronics	LAB	1		M	11:45 AM	2:35 PM		LGT ENGR LAB 283	David Westerf
ESE	272	Electronics	LAB	2		F	11:45 AM	2:35 PM		LGT ENGR LAB 283	David Westerf
ESE	272	Electronics	LAB	3		F	3:00 PM	5:50 PM		LGT ENGR LAB 283	David Westerf
ESE	272	Electronics	LAB	4		M	8:00 AM	10:50 AM		LGT ENGR LAB 283	David Westerf
ESE	273	Microelectronic Circuits	LEC	30	3	MW	2:40 PM	4:00 PM	80	FREY 317	Ridha Kamoua
ESE	300	Technical Communication	LEC	1	2	TU	8:15 PM	9:20 PM	65	LIBRRY W4550	Ronald Marge
ESE	301	Engineering Ethics	LEC	1	2	TUTH	8:00 AM	9:20 AM	80	FINART M0113	Donna Tummi
ESE	305	Deterministic Signals & Systms	LEC	1	3	TUTH	1:15 PM	2:35 PM	80	FREY 301	Vibha Mane
ESE	306	Random Signals & Systems	LEC	1	3	MW	6:05 PM	7:25 PM	80	UNION 103-02	Vibha Mane
ESE	315	Control System Design	LEC	1	3	MW	8:30 AM	9:50 AM		Harrmn 112	Ji Liu
ESE	324	Adv. Electronics Lab	LEC	1		TH	4:45 PM	5:40 PM	55	FREY 211	Emre Salman
ESE	324	Adv. Electronics Lab	LAB	L01	3	TU	4:45 PM	750PM	170	LGT ENGR 283	Emre Salman
ESE	324	Adv. Electronics Lab	LAB	L02	3	M	6:05 PM	9:00 PM	175	LGT ENGR 283	Emre Salman
ESE	324	Adv. Electronics Lab	LAB	L03	3	TH	6:30 PM	9:35 PM	185	LGT ENGR 283	Emre Salman
ESE	326/556	Algorithmic Electronic Design	LEC	1	3	MW	6:05 PM	7:25 PM	80	FREY 301	Alexa Doboli
ESE	330	Integrated Electronics	LEC	30	3	MW	11:45 AM	1:05 PM	80	FREY 105	Milutin Stanac

ESE	332	Quantum Mech. for Engineers	LEC	1	3	MW	2:40 PM	4:00 PM	80	FREY 105	Vera Gorfinkel
ESE	333	Real-Time Operating Systems	LEC	01		TH	6:30 PM	9:20 PM	185	LIBRRY N3063	Daniel Benz
ESE	343/543	Mobile Cloud Computing	LEC	1	3	TUTH	3:00 PM	4:20 PM	80	LIBRRY W4540	Shan Lin
ESE	344	Software Techniques for Engnrs	LEC	1	3	TUTH	11:30 AM	12:50 PM	80	FREY 105	Murali Subbar
ESE	345	Computer Architecture	LEC	1	3	MW	6:05 PM	7:25 PM	80	LTENGR 152	Mikhail Doroj
ESE	350	Electrical Power Systems	LEC	1	3	M	2:40 PM	5:30 PM	170	LGT ENGR 152	Timothy Drisc
ESE	355	VLSI System Design	LEC	1	3	MF	1:00 PM	2:20 PM		LGT ENGR 154	Alex Doboli
ESE	360	Network Security Engineering	LEC	1	3	TU	6:30 PM	9:20 PM	170	LIBRRY E4315	Carlos Gambo
ESE	381	Embedded Microproc Syst Des II	LEC	1		TUTH	9:45 AM	11:05 AM	80	LTENGR 152	Kenneth Short
ESE	381	Embedded Microproc Syst Des II	LAB	L01	4	TH	1:15 PM	4:15 PM	180	LTENGR 230	Kenneth Short
ESE	381	Embedded Microproc Syst Des II	LAB	L02	4	F	2:40 PM	5:30 PM	170	LTENGR 230	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LEC	1		TUTH	3:00 PM	4:20 PM	80	LTENGR 102	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LAB	L01	4	W	8:30 AM	11:20 AM	170	LTENGR Lab 228	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LAB	L02	4	W	2:40 PM	5:30 PM	170	LTENGR Lab 228	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LAB	L03	4	TH	8:00 AM	10:50 AM	170	LTENGR Lab 228	Kenneth Short
ESE	382	Digital Des Using VHDL & PLDs	LAB	L04	4	F	8:30 AM	11:20 AM	170	LTENGR Lab 228	Kenneth Short
ESE	411	Analog Integrated Circuits	LEC	1	3	MW	2:40 PM	4:00 PM	80	LIBRRY W4535	Dmitri Donets
ESE	441	Senior Design II	LEC	1		TU	4:45 PM	7:35 PM	170	HUMANE 1006	Harbans Dhad
ESE	441	Senior Design II	TUT	T04	3	FLEX	1:00 AM	1:00 AM	0		Harbans Dhad

ESE 442/506	Wireless Network	LEC		3	TU	4:45 PM	7:35 PM		FREY 305	Xin Wang	
ESE	451	Power Electronics	LEC	1	3	TUTH	1:15 PM	2:35 PM	80	LTENGR 248	Fang Luo
ESE	494	Honors Seminar on Research	LEC	1		M	2:40 PM	3:55 PM		SOCBEH N310	Vibha Mane
ESE	495	Honors Research Project	TUT	T03	3	APPT	1:00 AM	1:00 AM	0	LTENGR 231	
ESE	442/506	Wireless Network	LEC	1	3	TU	4:45 PM	7:35 PM	170	FREY 305	Xin Wang